

09/831525

756	Subclass
714	Class
ISSUE CLASSIFICATION	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>M.H.</i> <i>LM3</i> Q.A. <i>SS</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/831525	D F	714	736	2184	<i>BRITT</i> <i>Decady</i>

APPLICANTS

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TITLE

Method and installation for first fault localization in an integrated circuit

PTO-2040  
12/99

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ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
714	736	714	819				
INTERNATIONAL CLASSIFICATION		324	523				
G06F	H100	327	603				
G06F	7102						
G01R	3108						
G01R	3128						
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg. 13	Figs. Drwg. 20	Print Fig. 3	Total Claims 35	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Cynthia Beeth</i> (Assistant Examiner)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	<i>5/11/04</i> (Date)			<i>5/17/04</i>	
	<b>ALBERT DECADY</b> SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2109 <i>A. Decady</i> (Primary Examiner)			<b>ISSUE FEE</b>	
				Amount Due \$1330	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	

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